AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A content output apparatus that outputs any one of N contents (N: 2 or any larger integer) individually transmitted through N channels registered in a predetermined order, the content output apparatus comprising:

a ~~writing means~~ writer for respectively writing M contents (M: an arbitrary integer that is 2 or larger and N or smaller) transmitted through M channels that exist in [[a]] said predetermined order and include a ~~desired~~ predetermined channel into M buffer memories;

a ~~reading means~~ reader for reading a content that is transmitted through said ~~desired~~ predetermined channel from any one of said M buffer memories; and

an ~~accepting means~~ acceptor for accepting a change ~~changes of~~ from said ~~desired~~ predetermined channel to an other channel in said predetermined order, wherein

said reader changes, in response to said change, a target to be read from the buffer memory which is written with a first content that is transmitted through said predetermined channel to the buffer memory which is written with a second content that is transmitted through said other channel, and

said writer renews, in response to said change, the content written in the buffer memory that is apart from the buffer memory which is written with said second content by a predetermined number in said predetermined order to the other content.

Claim 2 (currently amended): A content output apparatus according to claim 1, wherein said ~~writing means~~ writer includes an ~~updating means~~ updater for updating any one of said M buffer memories in response to the change of said ~~desired~~ predetermined channel.

Claim 3 (currently amended): A content output apparatus according to claim 1, further comprising:

a ~~holding means~~ holder for holding a table in which said N channels are registered in said predetermined ~~sequence~~ order; and

a ~~specifying means~~ specifier for specifying said M channels by reference to said table held by said ~~holding means~~ holder.

Claim 4 (original): A content output apparatus according to claim 1, wherein said contents are steaming contents transmitted in real time.

Claim 5 (currently amended): A program storage medium readable by a content output apparatus, tangibly embodying a content output control program of instructions executable by the

content output apparatus to perform method steps such that the ~~to be executed by a~~ content output apparatus ~~that~~ outputs any one of N contents (N: 2 or any larger integer) individually transmitted through N channels registered in a predetermined order, ~~the content output control program~~ the method steps comprising:

a writing step of respectively writing M contents (M: an arbitrary integer that is 2 or larger and N or smaller) transmitted through M channels that exist in [[a]] said predetermined order and include a ~~desired~~ predetermined channel into M buffer memories, said writing step being performed by a writer;

a reading step of reading a content that is transmitted through said ~~desired~~ predetermined channel from any one of said M buffer memories, said reading step being performed by a reader; and

an accepting step of accepting ~~changes of~~ a change from said ~~desired~~ predetermined channel to an other channel in said predetermined order, wherein

said reader changes, in response to said change, a target to be read from the buffer memory which is written with a first content that is transmitted through said predetermined channel to the buffer memory which is written with a second content that is transmitted through said other channel, and

said writer renews, in response to said change, the content written in the buffer memory that is apart from the buffer memory which is written with said second content by a predetermined number in said predetermined order to the other content.

Claim 6 (currently amended): A content output control method to be practiced by a content output apparatus that outputs any one of N contents (N: 2 or any larger integer) individually transmitted through N channels registered in a predetermined ~~sequence~~ order, the content output control method comprising:

a writing step of respectively writing M contents (M: an arbitrary integer that is 2 or larger and N or smaller) transmitted through M channels that exist in [[a]] said predetermined order and include a ~~desired~~ predetermined channel into M buffer memories, said writing step being performed by a writer;

a reading step of reading a content that is transmitted through said ~~desired~~ predetermined channel from any one of said M buffer memories, said reading step being performed by a reader; and

an accepting step of accepting ~~changes of~~ a change from said ~~desired~~ predetermined channel to an other channel in said predetermined order, wherein

said reader changes, in response to said change, a target to be read from the buffer memory which is written with a first content that is transmitted through said predetermined channel to the buffer memory which is written with a second content that is transmitted through said other channel, and

said writer renews, in response to said change, the content written in the buffer memory that is apart from the buffer memory which is written with said second content by a predetermined number in said predetermined order to the other content.

Claim 7 (currently amended): A content output control method according to claim 6, wherein said reading step includes a changing step of, when the change of said ~~desired~~ predetermined channel is accepted in said accepting step, changing a buffer memory from which a content is to be read.

Claim 8 (currently amended): A content output control method according to claim 6, wherein said writing step includes a replacing step of, when the change of said ~~desired~~ predetermined channel is accepted in said accepting step, replacing any one of said M channels with any one of channels that are included in said N channels and are not included in said M channels.

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